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Senior Digital Design Engineer

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Location: Spain

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Exciting opportunity to work on the latest cutting edge technology in the semiconductor industry.

In this new role as senior digital design engineer you will have the opportunity to contribute to advanced technology nodes, consisting of RISC-V designs, ARM & CPU architecture, PCIe protocols and machine learning.

I am looking to speak with senior digital design engineers - who have experience of, or an interest in:
Degree in Electronics / Microelectronics or similar field

5+ years' experience in ASIC / FPGA design projects

UVM environments & processes

System Verilog for IP / SOC design of digital ICs / ASIC IP or chips

Complex ASIC/FPGA designs & architecture for advanced technology nodes

RISC-V / CPU / GPU knowledge (is a bonus, but not required)

PCIe knowledge (also a bonus skill)

scripting / coding skills - C/C++, python etc.

Visa sponsorship and relocation support can be offered if required (dependent on experience/qualifications)

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